IN THE UNITED STATES PATENT AND TRADEMARK OFFICE.

In re application of Docket No: Q79257

Katsuki KUSUNOKI

Appln. No.: 10/581,335 Group Art Unit: 2829

Confirmation No.: 6238 Examiner: Shantanu Pathak

Filed: December 14, 2006

For: METHOD FOR PRODUCTION OF SEMICONDUCTOR CHIP, AND

SEMICONDUCTOR CHIP

RESPONSE UNDER 37 C.F.R. § 1.116

MAIL STOP AF

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated December 10, 2009, please consider the remarks regarding the above-identified application as follows on the accompanying pages. A Petition for Extension of Time for one month is concurrently submitted extending the time for responding to April 10, 2010. April 10, 2010 falls on a Saturday; the present submission filed Monday, April 12, 2010 is timely.

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